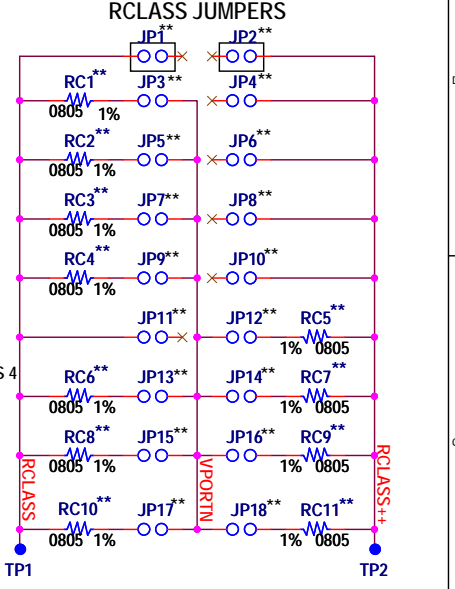


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	PRODUCTION	KAUNG H.	01-07-13

JUMPER SELECTIONS				
ASSY	POE CLASS	POWER	CLASS	CLASS ++
A,C	0	13W	JP1	JP2
A,C	1	3.84W	JP3	JP4
A,C	2	6.49W	JP5	JP6
A,C	3	13W	JP7	JP8
A	4 ***	25.5W	JP9	JP10
A	4 ***	38.7W	JP11	JP12
A	4 ***	52.7W	JP13	JP14
A	4 ***	70W	JP15	JP16
A	4 ***	90W	JP17	JP18

*** AN LTPOE ++ PD WILL BE CLASSIFIED AS CLASS 4 BY AN IEEE 802.3 AT COMPLIANT PSE.



NOTE 1: UNLESS OTHERWISE SPECIFIED

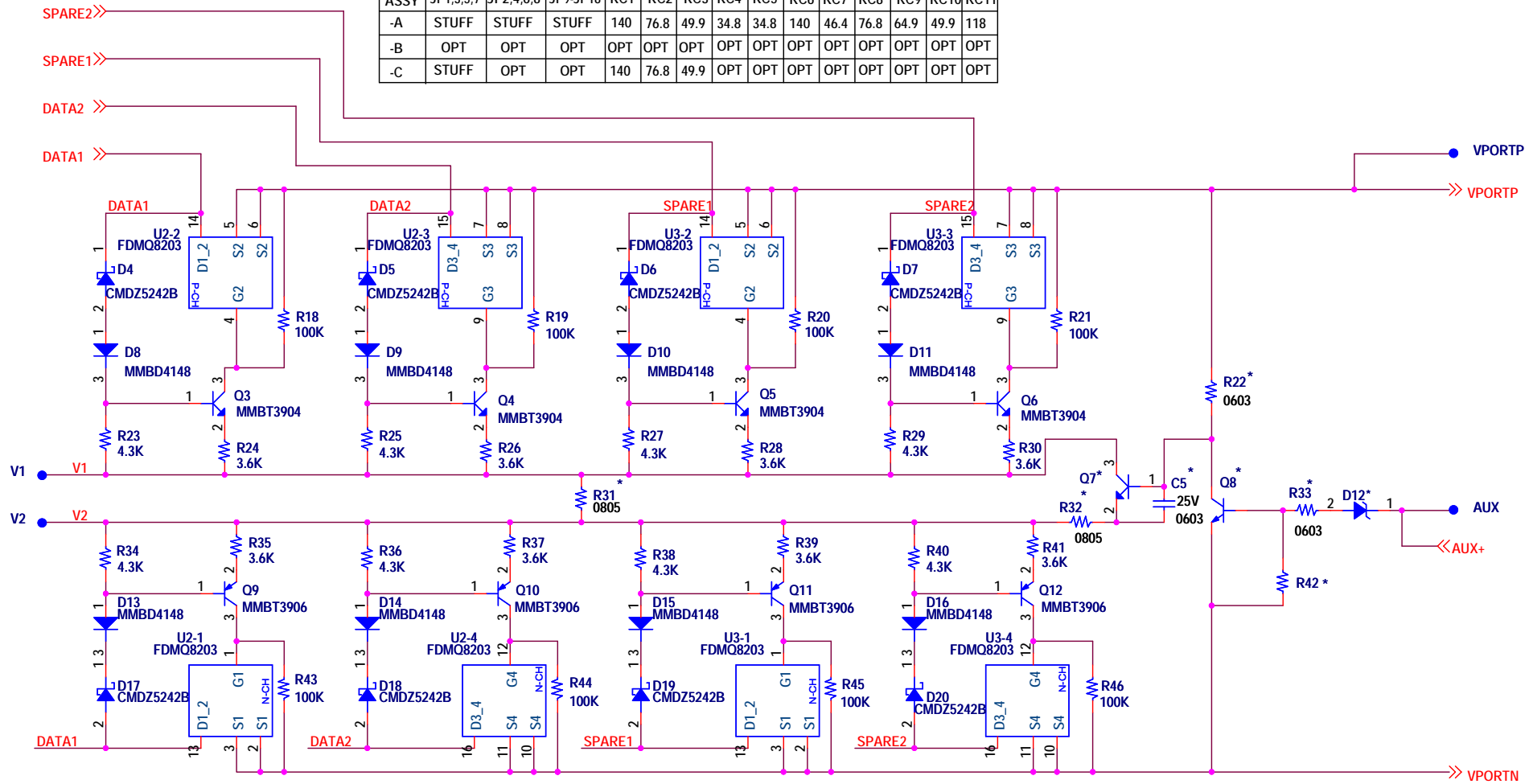
- ALL RESISTORS AND CAPACITORS ARE 0603
- ALL RESISTORS TOLERANCE RATING ARE 5%

* SEE OPTION TABLES ON SHEET 2

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.	APPROVALS <table border="1"> <tr> <td>PCB DES.</td> <td>KAUNG H.</td> </tr> <tr> <td>APP ENG.</td> <td>KAUNG H.</td> </tr> <tr> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> </tr> </table>	PCB DES.	KAUNG H.	APP ENG.	KAUNG H.						1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only
PCB DES.	KAUNG H.										
APP ENG.	KAUNG H.										
TITLE: SCHEMATIC											
LTPOE ++ POWERED DEVICE											
SIZE N/A	IC NO. LT4275XIDD DEMO CIRCUIT 1788B	REV. 1									
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.	SCALE = NONE	DATE: Monday, January 07, 2013									
		SHEET 1 OF 2									

★ ASSY	U1	E3	E5	E6	T1	D1	D12	Q1	Q2	Q7, Q8	C5	R9	R10	R15	R16	R22	R32	R33	R31	R42	RMPS1
-A	LT4275AIDD	STUFF	OPT	OPT	749022016	OPT	OPT	FDMC86102	OPT	OPT	OPT	OPT	0	100K	OPT	OPT	OPT	OPT	10K	OPT	5.1K
-B	LT4275BIDD	STUFF	STUFF	STUFF	749022017	B1100A	CMHZ5256B	OPT	FDN8601	MMBTA42	0.1uF	82.5K	20K	100K	34.8	100K	10K	56K	OPT	62K	4.7K
-C	LT4275CIDD	OPT	STUFF	STUFF	749023015	B1100A	CMHZ5256B	OPT	FDN8601	MMBTA42	0.1uF	82.5K	20K	OPT	OPT	100K	10K	56K	OPT	62K	4.3K

** ASSY	JP1,3,5,7	JP2,4,6,8	JP9-JP18	RC1	RC2	RC3	RC4	RC5	RC6	RC7	RC8	RC9	RC10	RC11
-A	STUFF	STUFF	STUFF	140	76.8	49.9	34.8	34.8	140	46.4	76.8	64.9	49.9	118
-B	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT	OPT
-C	STUFF	OPT	OPT	OPT	140	76.8	49.9	OPT	OPT	OPT	OPT	OPT	OPT	OPT



NOTE 2: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE 0402
2. ALL RESISTORS TOLERANCE RATING ARE 5%

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	KAUNG H.
APP ENG.	KAUNG H.

SCALE = NONE



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TITLE: SCHEMATIC

LTPOE ++ POWERED DEVICE

SIZE N/A	IC NO. LT4275XIDD DEMO CIRCUIT 1788B	REV. 1
DATE: Monday, January 07, 2013	SHEET 2 OF 2	